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Approved for use through 10/31/2002, OMB 051-0031

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Complete if Known

Application Number	09/917,257
Filing Date	July 27, 2001
First Named Inventor	Rumynin, Dmitriy
Group Art Unit	2193
Examiner Name	Malzahn, David

Sheet 1 of 1

Attorney Docket No: 1365.051US1

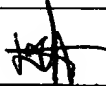

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-6,883,011	04/19/2005	Rumynin, D. , et al.	01/25/2001

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Communication Pursuant to Article 96(2) EPC, for application No. EP 02 722 402.1, date mailed June 6, 2005", 3 Pages	
		SONG, PAUL J., et al., "Circuit and Architecture Trade-offs for High-Speed Multiplication", IEEE Journal of Solid-State Circuits, Vol. 26, No. 9, (September 1991), 1184-1198	

EXAMINER

D H. Malzahn

DATE CONSIDERED

5/1/06

Substitute Disclosure Statement Form (PTO-1448)

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